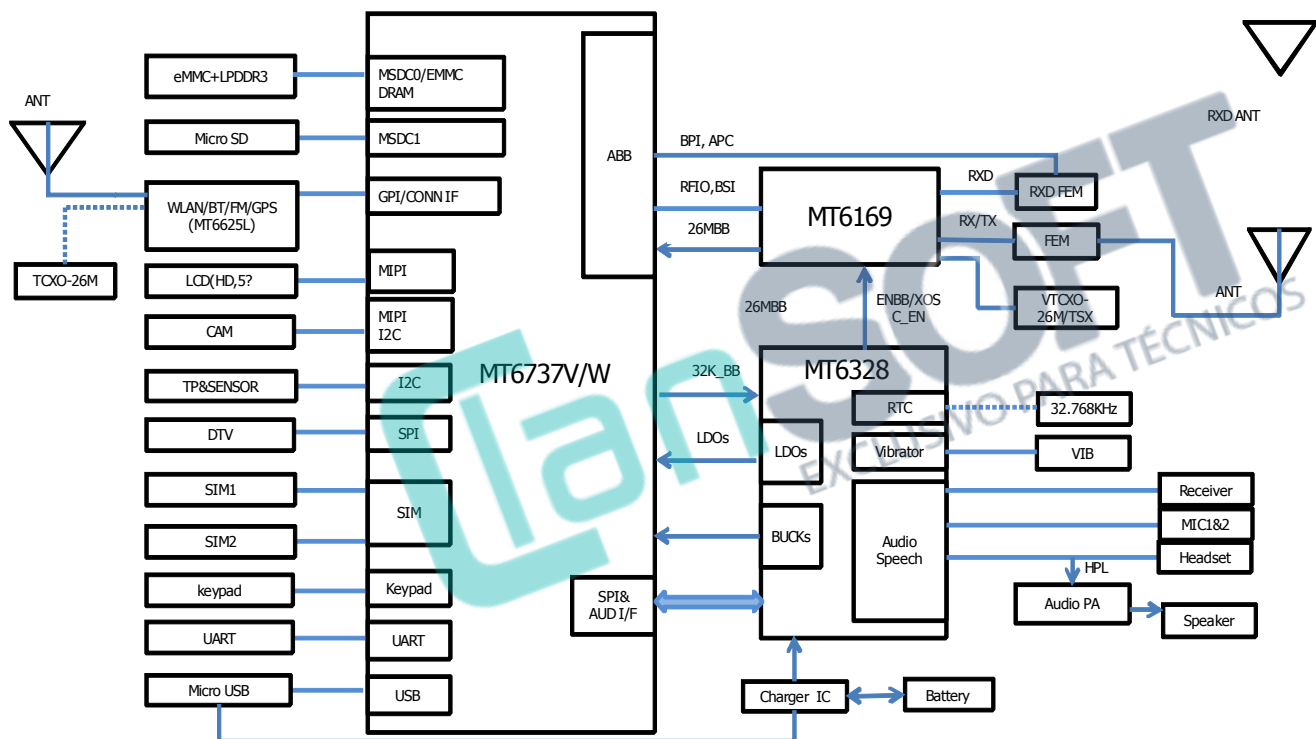
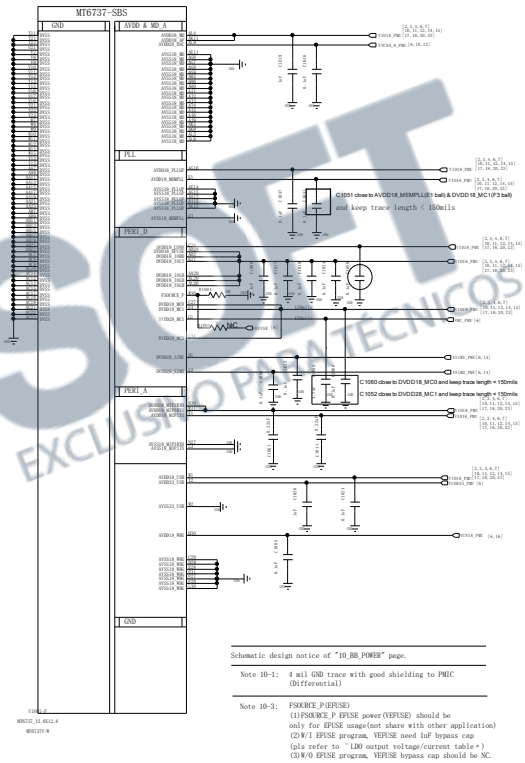
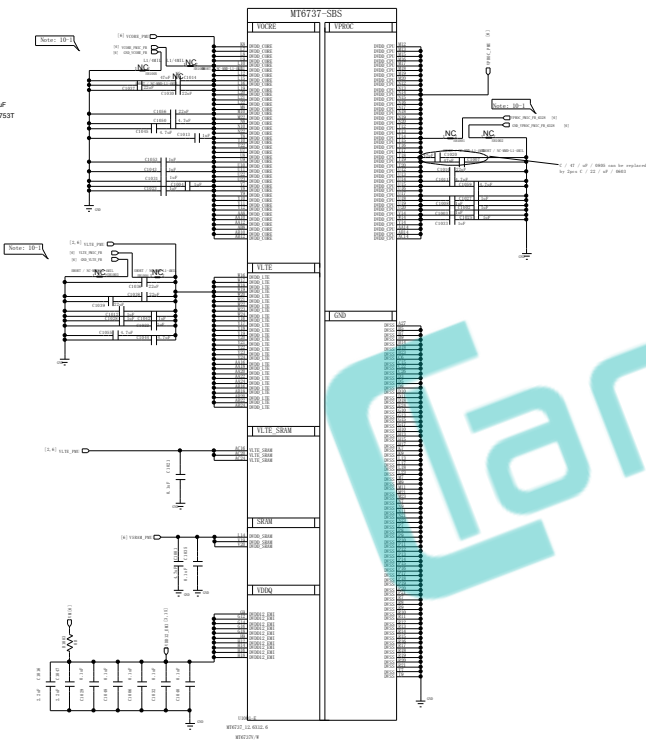


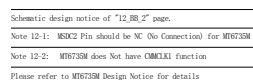


Title	01_BLOCK_DIAGRAM		
Size	D	Yude Confidential	
Date	Sep 14, 2016	Sheet	11 of 99

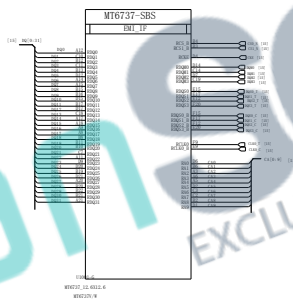
C1014 should be 47uF
for MT6753 and MT6753T

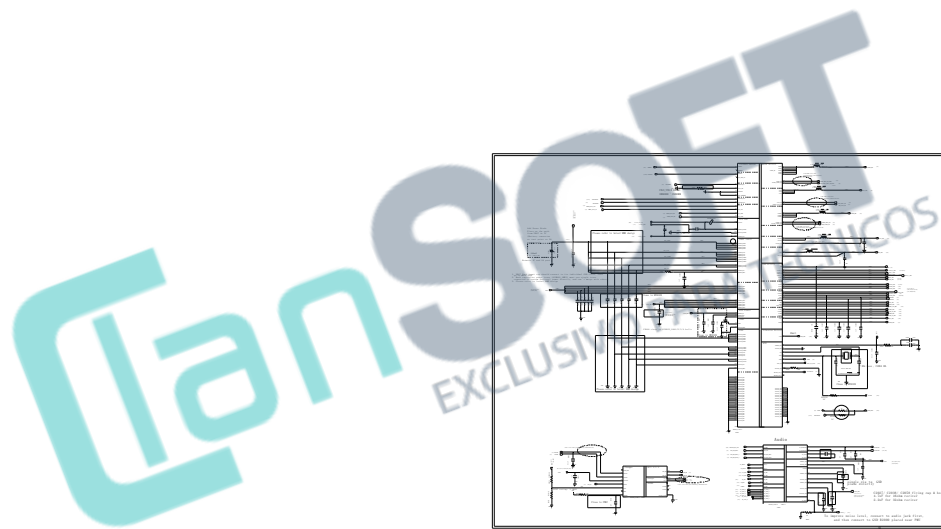


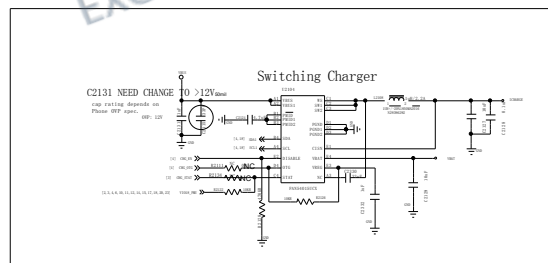
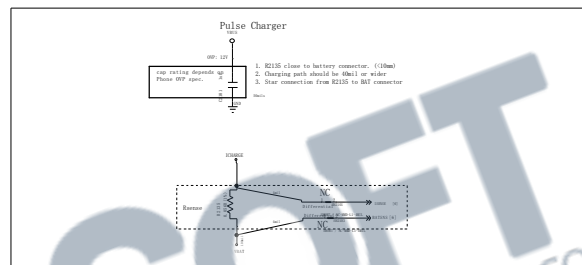
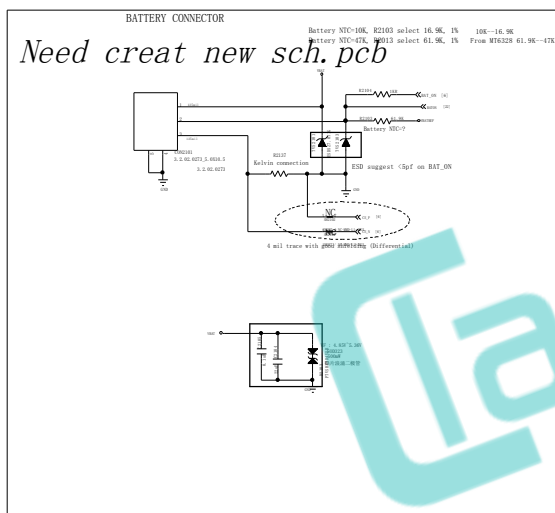
Title			10_BB_POWER
Size			D Yude Confidential
Data			Sep 14, 2016 Sheet 10 of 99



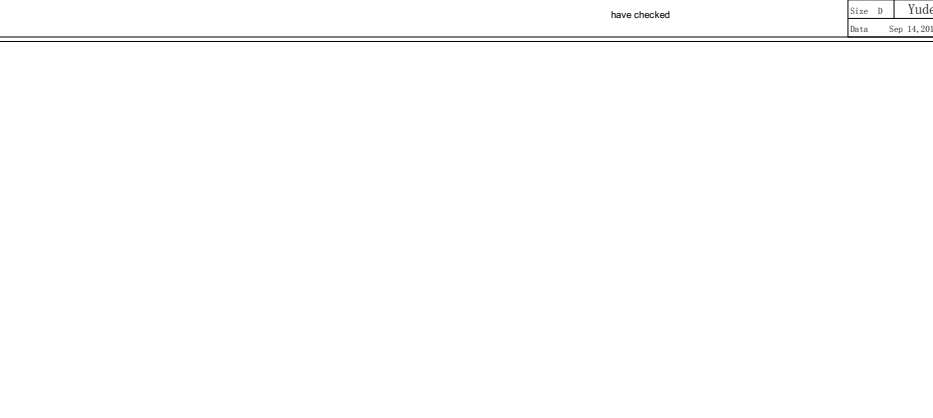
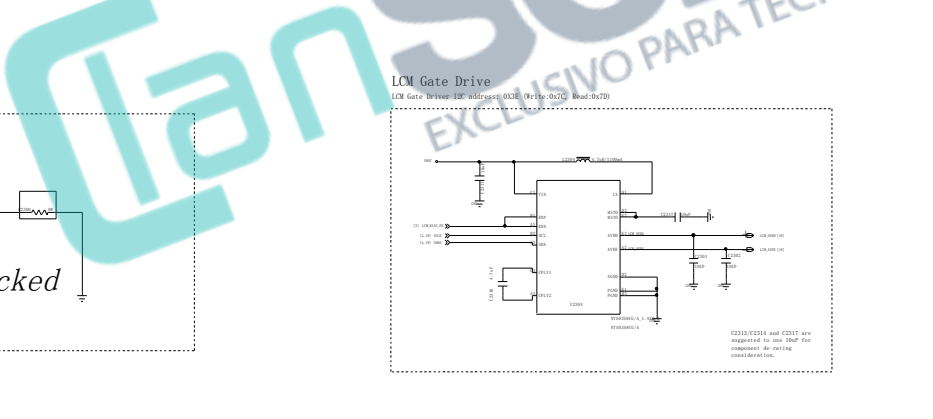
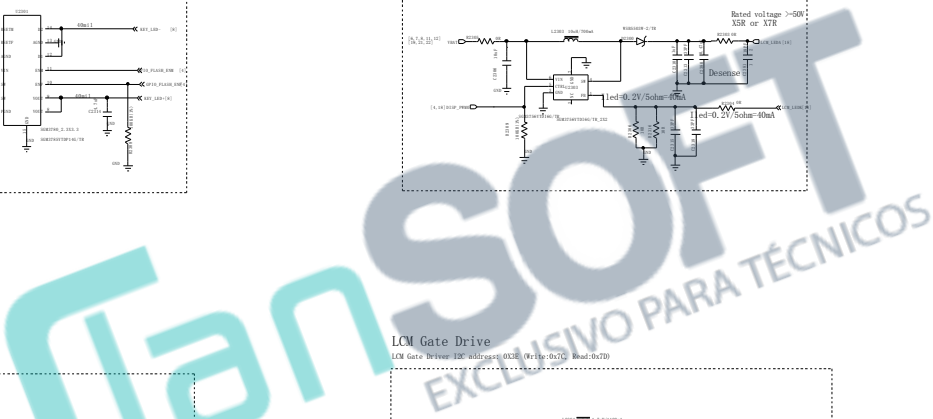
Title 12_BB_2			
Size D		Yude Confidential	
Data Sep 14, 2016		Sheet 11 of 99	







Title		21_POWER_BATTER_CHARGE	
Size		D Yude Confidential	
Data	Sep 14, 2016	Sheet	21 of 99



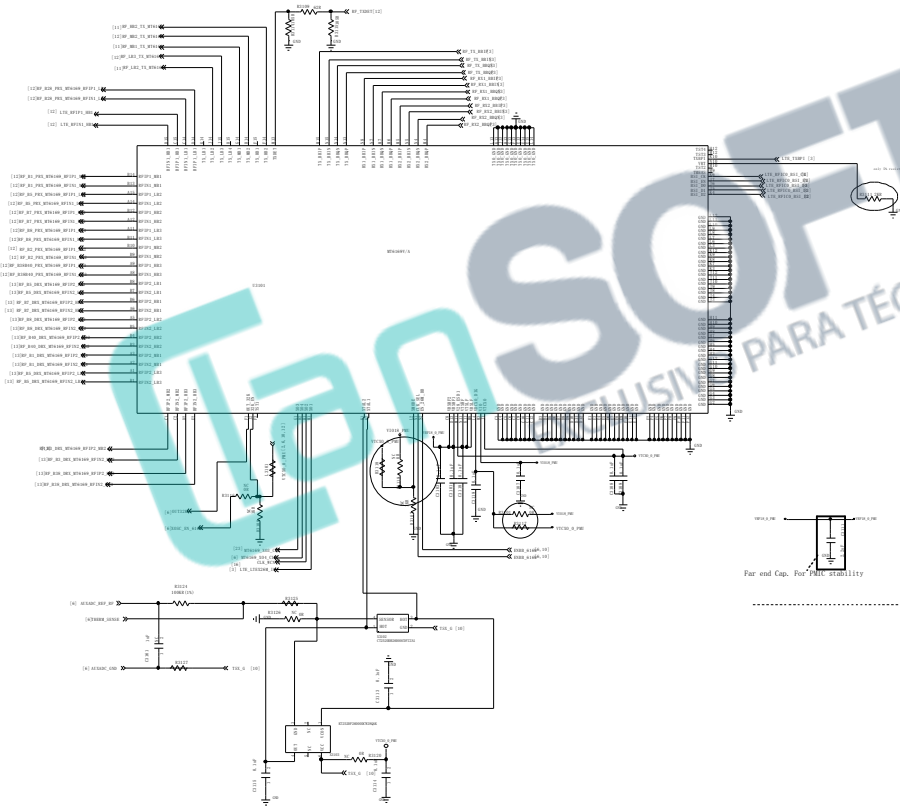
Title		23_POWER_FLASH_LCM BL	
Size	D	Yude Confidential	
Data	Sep 14, 2016	Sheet	11 of 99

External Buck for DRAM

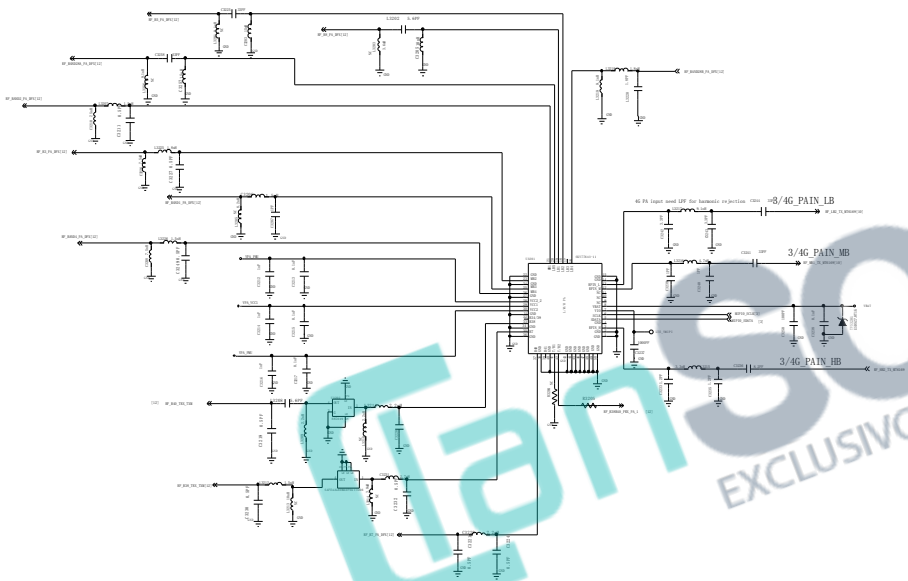
If using external buck for DRAM application, please refer to "MT8735_53 DRAM External Buck Design Notice":
1. Please add one more 1uF capacitor on PMIC VM output side (Must)
2. Please turn off Fast Transient Function of PMIC VM LDO (Must)

Title			24_NONE		
Size		D	Yude Confidential		
Data		Sep 14, 2016	Sheet		11 of 99

Power trace	PCB line width	MAX current
VREF18_0_PWE	0.4mm	
VIO18_PWE	0.35mm	
VTCX0_0_PWE	0.2mm	



Title	31_RF MT6169 PIN_OUT
Size D	Yude Confidential
Data	April 5, 2015 Sheet 31 of 99

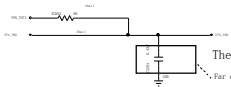


Power trace	PCB line width	MAX current
VPA_PMI	1.1mm	
VPA_NCC1	0.5mm	
VBAT	0.15mm	
LTE_VMIPI	0.15mm	

Thermistor / To sense board level temperature



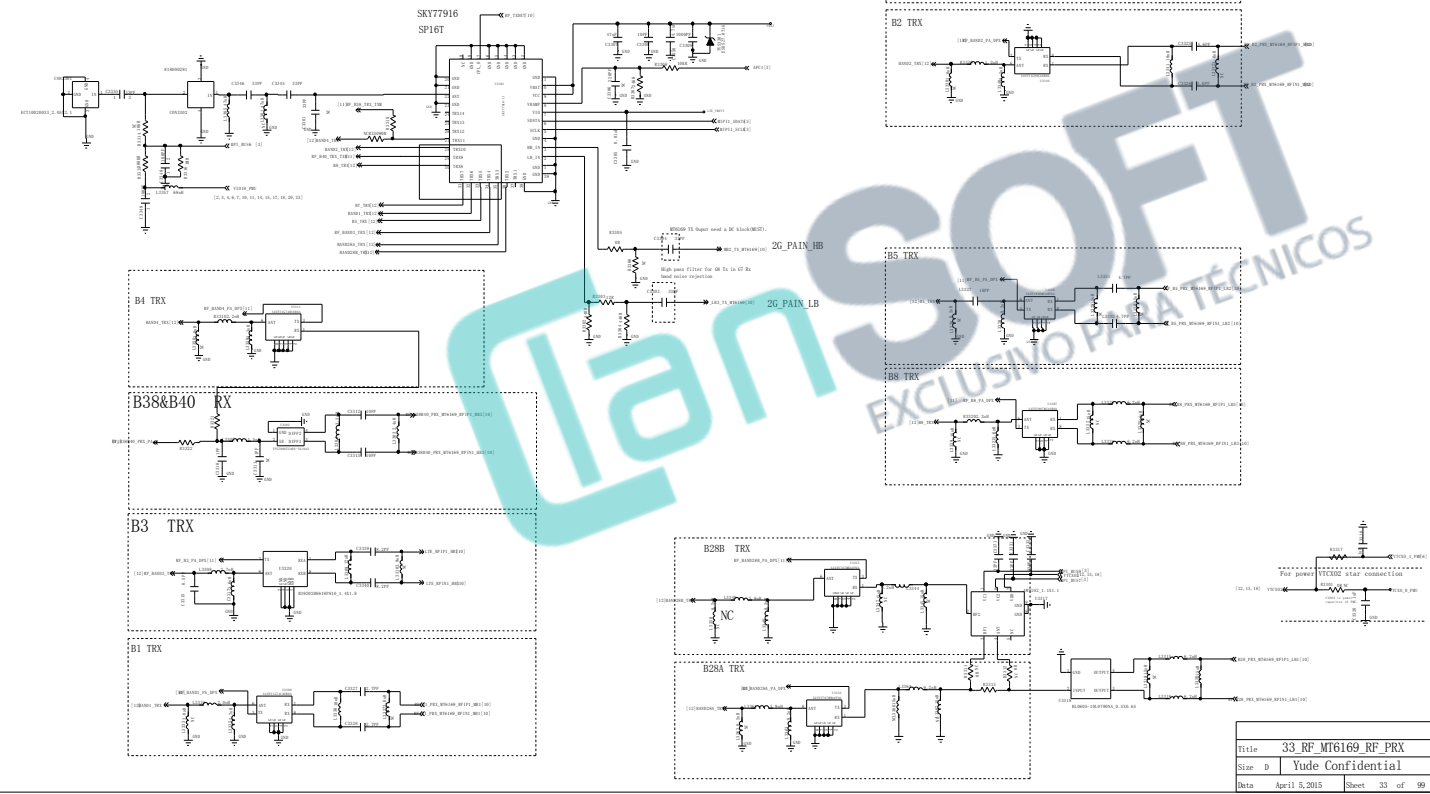
VPA_NCC1 and VPA_PMI need to be connected after Cap



Thermistor / To sense board level temperature
Far end Cap. For PMIC stability

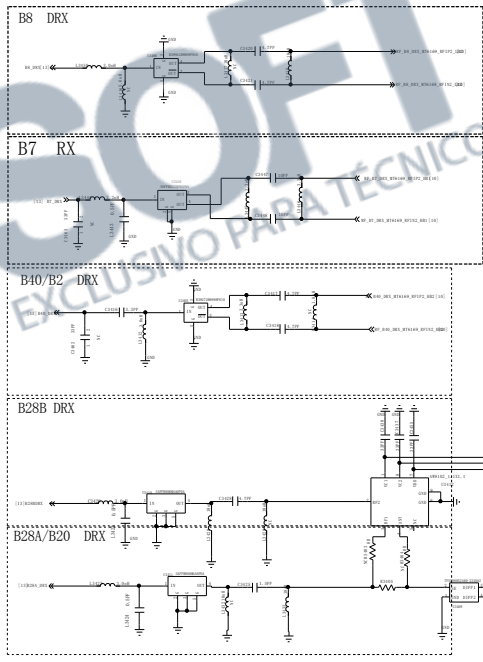
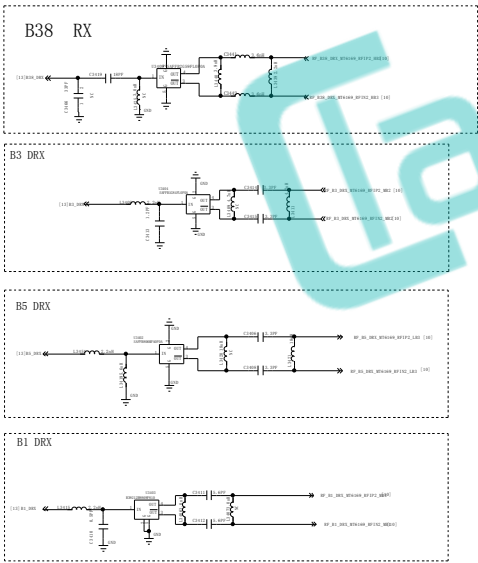
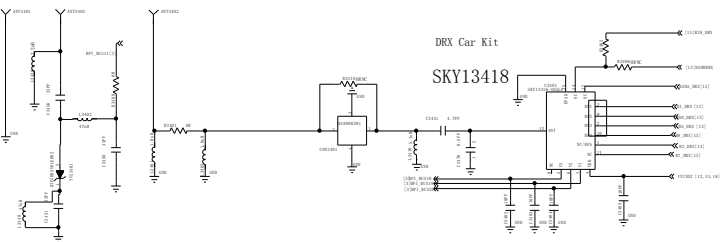
Title	32_RF_MT6169_RF_TX
Size	0 Yude Confidential
Date	April 5, 2015
Sheet	32 of 99

Power trace	PCB line width	MAX current
VDDT	2.5mm (Min 2.0mm)	
LTE_VDDP1	0.15mm	



Title	33 RF MT6169 RF PRX
Size	Yude Confidential
Date	April 5, 2015
Sheet	23 of 99

Power trace	PCB line width	MAX current
VTC002	0.15mm	50uA



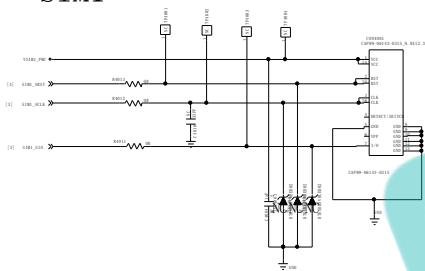
Title	34_RF_MT6169_RF_DRX
Size	D
Data	Nov 6, 2015
Sheet	34 of 99

SIM1_sclk & SIM2_sclk sheilding by GND

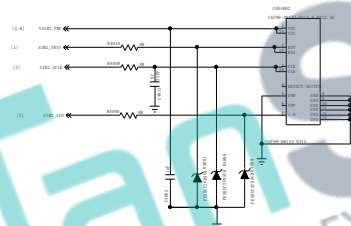
SIM1

SIM2

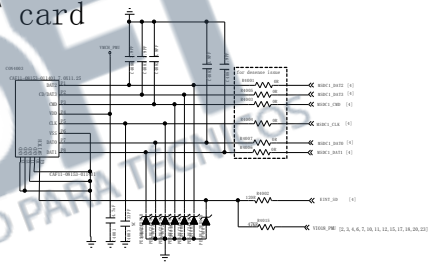
TF card



checked



checked



checked

Title 40 MEMORY SD CARD SIMCARD		
Size D	Yude Confidential	
Date	Sep 14, 2016	Sheet 40 of 99

Speaker NC

Handset Microphone 1

Close to MIC

Close to IC

0.250V 01

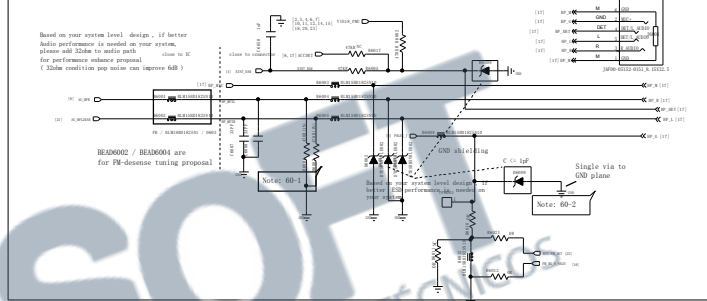
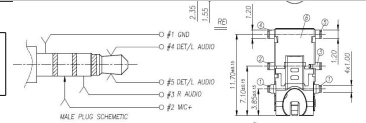
0.250V 01

Reserved for AEC mode

Based on your system level design, if better audio performance is needed on your system, please reserve for AEC mode.

checked

DIMENSION IN mm		LCN Shenzhen Linkconn Electronics Co., Ltd.	
TOLERANCE UNLESS OTHERWISE SPECIFIED		TYPE	#1.5 AUDIO JACK (DIP) INSERT TYPE
J04-0.35	X 4' 5'	DATE	JAF00-05152-0151
J04-0.25	X 2' 5'	PRICE	CUSTOMER DATA
J004-0.35	J04' 1'	REV. A-1	DATE 3-1-01

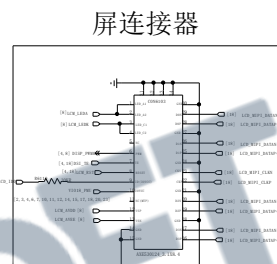
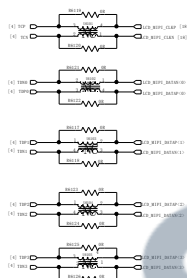
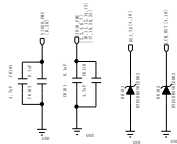
[illegible]

Receiver

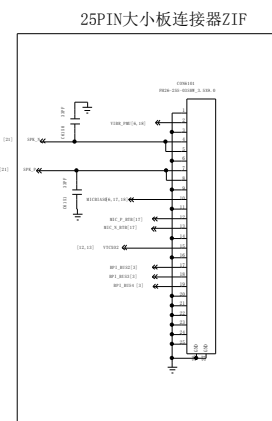
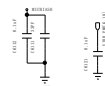
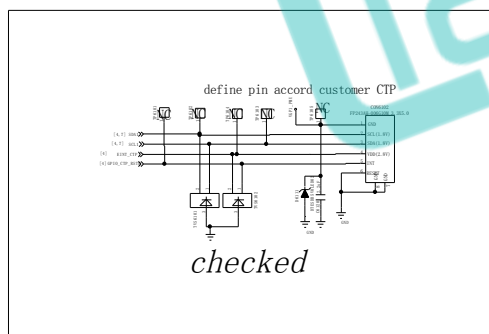
close to TX
close to connector

Based on your system level design better ESD performance is needed in your system.

Title		60 PERI AUDIO IO	
Size	D	Yude Confidential	
Data	Sep 14, 2016	Sheet	60 of 99



530124 or 530127???



Title	61_PERI_LCD_CTP
Size	D
Data	Sep 14, 2016
Sheet	61 of 99

camera需要参考最新的部件

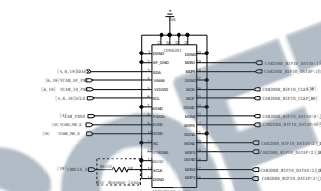
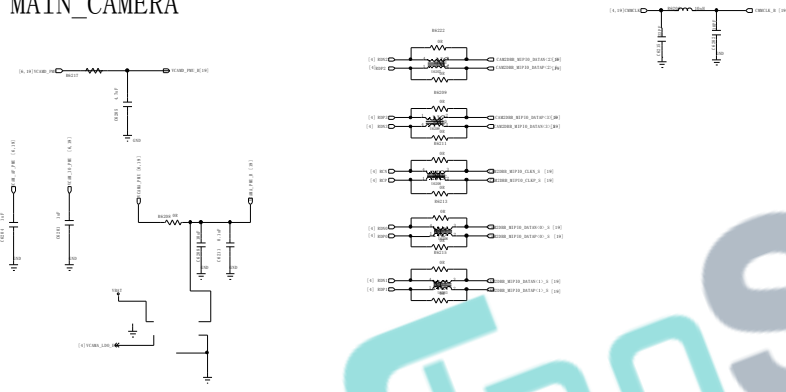


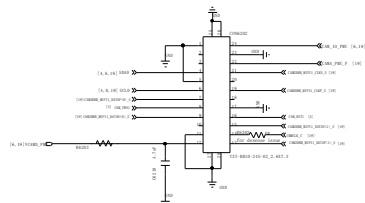
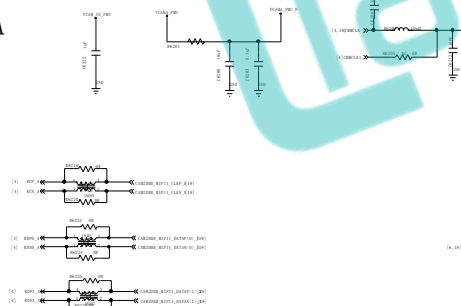
Diagrama de bloques de un sistema de control de velocidad en cascada:

El sistema de control de velocidad en cascada se compone de los siguientes bloques:

- Controlador de velocidad (PID) con ganancias K_p , K_i y K_d .
- Subsistema de control de corriente.
- Motor.

Las variables de entrada y salida son:

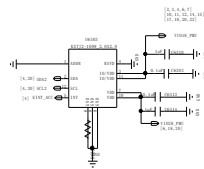
- Entrada: Referencia de velocidad (V_{ref}).
- Salida: Velocidad real (V_{real}).
- Variables internas: Corriente de armadura (I_a), Tensión de alimentación (V_a).



Need creat new sch, pcb

Title		62_PERI_CAMERA	
Size	D	Yude Confidential	
Data	Sep 14, 2016	Sheet	11 of 99

G Sensor-3X



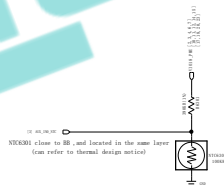
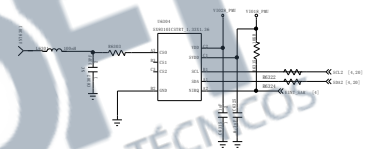
CS=1: IIC
 LIS3DHTR : SAM-0: IIC ADDRESS is 0x18H
 KXCJK-1113 : SAM-0: IIC ADDRESS is 0x1BH
 power down(communicating), I = 0.5uA

Configuration VS Chip

Fuction	U3001	C3004	C3005	C3006	R3003
G-Sensor	LIS3DHTR	NC	NC	NC	0
G-Sensor	KXCJK-1113	NC	NC	NC	0

Default LIS3DHTR

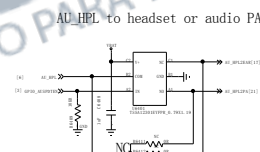
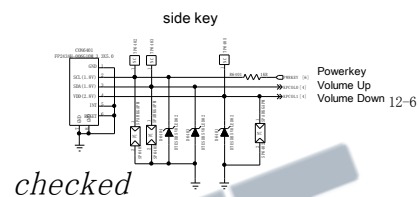
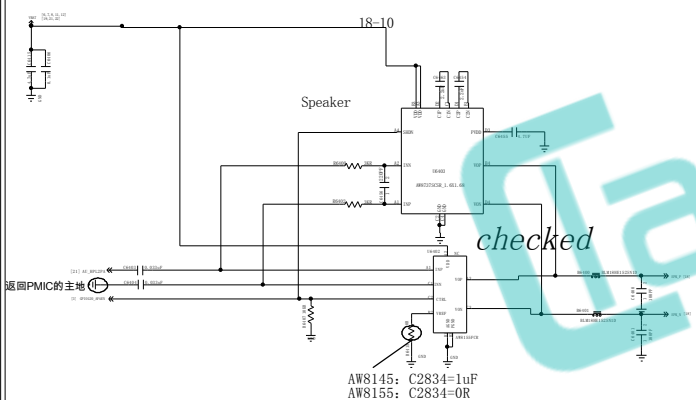
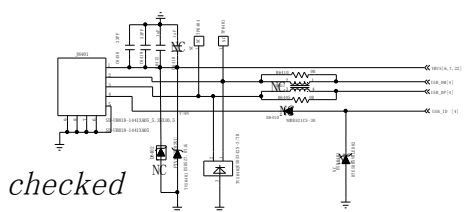
SAR Sensor



Thermistor / To sense board level temperature

have checked

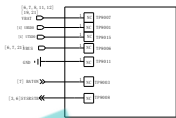
Title	63_PERI_SENSORS
Size	D
Size	Yude Confidential
Date	Sep 14, 2016
Sheet	11 of 99



Title	64_PERI_USB_KEYPAD
Size	D
Data	Sep 14, 2016
Sheet	11 of 99

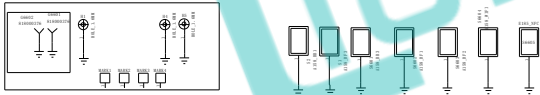
UART只是测试不按照生产要求
开关机键电源键，USB按照生产要求摆放2.5mm

TEST POINTS



NC

接地弹片改818000376



have checked

Title			65_TEST POINT		
Size		D		Yude Confidential	
Data		Sep 14, 2016		Sheet 11 of 99	

